Serial No.: 10/596,118

Filing Date: 5/31/2006 Attorney Docket No. 515.040US01

Title: TEST APPARATUS FOR CONTROL UNIT, PATTERN SIGNAL CREATING

APPARATUS, AND TEST PROGRAM GENERATING APPARATUS

## **Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

## **Listing of claims:**

- 1.-9. (Canceled)
- 10. (Currently amended) A test apparatus for a control unit, comprising:
  testing means for testing the operation of said control unit based on a
  relationship between a pattern signal input to said control unit and an output signal output
  in response to said pattern signal from a target being controlled by said control unit; and
  means for causing said testing means during execution of said a first
  pattern signal to switch to the execution of another a second pattern signal when a first
  pattern signal transition condition for making a transition to the execution of said other
  second pattern signal holds; and

means for causing said testing means during execution of said first pattern signal to switch to the execution of a third pattern signal when a second pattern signal transition condition for making a transition to the execution of said third pattern signal holds.

## 11.-19. (Canceled)

20. (Currently amended) A test method for testing the operation of a control unit based on a relationship between a pattern signal input to said control unit and an output signal output in response to said pattern signal from a target being controlled by said control unit, said method comprising;

a step for executing said a first pattern signal; and

Serial No.: 10/596,118

Filing Date: 5/31/2006 Attorney Docket No. 515.040US01

Title: TEST APPARATUS FOR CONTROL UNIT, PATTERN SIGNAL CREATING

APPARATUS, AND TEST PROGRAM GENERATING APPARATUS

a step for switching, during execution of said <u>first</u> pattern signal, to the execution of <u>another a second</u> pattern signal when a <u>first</u> pattern signal transition condition for making a transition to the execution of said <u>other second pattern</u> signal holds; <u>and</u>

a step for switching, during execution of said first pattern signal, to the execution of a third pattern signal when a second pattern signal transition condition for making a transition to the execution of said third pattern signal holds.